|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Cache** | **Cache Line Size** | **Total Size** | **Number of Ways (Associativity)** | **Number of Sets** | **Raw Latency** |
| L1 | 64 bytes | 32768 bytes | 8 | 64 | https://en.wikichip.org/wiki/intel/core\_i7/i7-9700 |
| L2 | 64 bytes | 262144 bytes | 4 | 1024 | https://en.wikichip.org/wiki/intel/core\_i7/i7-9700 |
| L3 | 64 bytes | 12582912 bytes | 12 | 16,384 | https://en.wikichip.org/wiki/intel/core\_i7/i7-9700 |

1.1

Cant find the raw latency online as said

latency = time\_of\_hit + p\_of\_miss \* time\_of\_miss